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JC690 U.S. PTO

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
UTILITY PATENT APPLICATION TRANSMITTAL UNDER 37 CFR 1.53(b)**

Address to:

Assistant Commissioner for Patents

Box Patent Application

Washington, DC 20231

Attorney Docket No.

2207/10083

Inventor(s)

TALAL K. JABER

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Total Pages

27

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Title of Application:

MICROPROCESSOR ON-CHIP TESTING ARCHITECTURE AND IMPLEMENTATION

Transmitted with the patent application are the following:

2 Page(s) Transmittal form (and one copy)
17 Page(s) Cover Page (1), Specification (11), claims (4), abstract (1)
4 Page(s) Informal drawing
2 Page(s) Declaration and Power of Attorney (signed)
2 Page(s) Recordation of Assignment and Assignment form
 Page(s) Information Disclosure Statement (IDS) (copies of citations not included in number of pages)
 Page(s) Certified copy of:

This application is a Continuation / Continuation-in-Part / Divisional of prior application Serial No. , filed .

Fee calculation for large entity:

	No. Filed	No. Allowed	No. Extra	Rate	Fee
Basic Fee					\$710.00
Total Claims	20	20	0	× 18.00	
Independent Claims	8	3	5	× 80.00	\$400.00
Multiple Dependent Claim				+ 260.00	
				Assignment	\$40.00
				Total	\$1,150.00

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or arising during the pendency of this application per 37 CFR §1.16–1.21, or to credit any overpayments, to Deposit Account 11-0600.

Express Mail Certificate

I hereby certify that the above paper/fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, DC 20231

Date of deposit: **December 29, 2000**

Person mailing paper/fee: Pilar Rodriguez

Signature

Pilar Rodriguez

Respectfully submitted,

Shawn W. O'Dowd

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